



# PMB072T060SS-255A

## 5A/60V<sup>(1)</sup>, low VF Planar MOS barrier diode

### Mechanical Data

Chip Drawing	Item	Information	
	Die Size (A)	1828μm	72.0 mil
	Top Metal Pad Size (B)	1728μm	68.0mil
	Chip Size (C)	/	/
	Wafer Thickness (D)	255 μm	10.0 mil
	Scribe Line Width (E)	80 μm	3.15 mil
	Wafer Size	6 inch	
	Top Side Metallization	PMB072T060SS-255A	Ag
	Back Side Metallization	Ti Ni Ag	
	Recommended Storage Environment	Stored in original container, in dry nitrogen, (6 months at an ambient temperature of 23°C±3°C)	

### Electrical Characteristics (T<sub>J</sub>=25°C, unless otherwise specified) <sup>(2)</sup>

Parameter	Description	Min.	Typ.	Max.	Unit	Test Condition
V <sub>BR</sub>	Reverse Breakdown Voltage	62	67	-	V	I <sub>R</sub> =300μA
V <sub>F</sub>	Instantaneous Forward Voltage	-	0.46	0.50	V	I <sub>F</sub> =5A <sup>(3)</sup>
I <sub>R</sub>	Reverse Leakage Current	-	50	120	μA	V <sub>R</sub> =60V
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature	-40°C to 150°C Max				

#### Note:

(1) The preliminary wafer datasheet only for reference;

(2) This characteristics assumes the dies are assembled in SMB packages. Actual performance may degrade when assembled. YJ does not guarantee device performance after assembly;

(3) Pulse Width t<sub>p</sub> = < 300μS, Duty Cycle <2%;